

**Amendments to the Drawings**

Attached are replacement drawing sheets for amended Figures 1 and 5.

**REMARKS**

The abstract, specification and claims have been amended in order to correct grammatical and idiomatic errors contained therein. No new matter has been added.

Enclosed herewith for the Examiner's approval are corrected Figures 1 and 5 in which the legend "Prior Art" has been added thereto. Entry of these drawings is respectfully solicited.

Acknowledgement is made of the Examiner's indication that Claims 18-23 are allowable. These claims have been amended only to correct possible formal defects contained therein and no substantive changes have been made thereto.

Acknowledgement is also made of the Examiner's indication that Claims 3, 4 and 13-17 contain allowable subject matter. In order to expedite the prosecution of the present application, the subject matter of objected-to Claim 3 has been incorporated into Claims 1 and 2. Accordingly, it is respectfully submitted that Claims 1 and 2 are now allowable. The subject matter of objected-to Claim 3 has also been incorporated into Claims 5 and 6. It is respectfully submitted that these claims are also in condition for allowance. No new matter has been added. It is respectfully submitted that the currently presented claims are cured of all formal defects.

Claims 1, 5, 7 and 11 have been rejected under 35 USC 102(b) as being anticipated by Goldstein. Claims 2, 6, 10 and 12 have been rejected under 35 USC 103(a) as being unpatentable over Goldstein in view of Fujimura et al. Claim 8 has been rejected under 35 USC 103(a) as being unpatentable over Goldstein in view of Matsuura et al. Applicants respectfully traverse these grounds of rejection and urge reconsideration in light of the following comments.

In view of the amendments to the claims, the only currently pending claims which have not been amended to contain allowable subject matter as deemed by the Examiner are

Claims 9-12. Claims 9-12 are directed to imaging quality correcting circuits which either comprise an occurrence frequency counter or a mean value computer. In the image quality correcting circuits of these claims, it is required that the occurrence frequencies of luminance levels be counted within N number of frames or within a N-frame period, in which N is an integer of at least 2. Claims 9-12 have been rejected over either the Goldstein reference or Goldstein in view of Fujimura et al.

The Goldstein reference discloses systems and methods for computing and applying a real-time transformation between digital input and output signals in order to achieve a specified output histogram. The Examiner states in the Office Action that Goldstein shows in Figure 6 and in column 3, lines 32-40, how the counted value of the occurrence frequency is corrected by the variation controller 12 using a linear interpolator or corrective curve generator, which sets new values within a period of several frames. Applicants have reviewed Figure 6, column 3, lines 32-40, and the entire disclosure of Goldstein and can find no description which suggests the method disclosed there could be used within a period of several frames. If the Examiner maintains his position, he is respectfully requested to be more detailed in his explanation as to how "several frames" is disclosed in Goldstein.

The Fujimura et al reference has been cited by the Examiner as teaching a video contrast enhancer including a histogram processor. However, the Fujimura et al reference does not make up the deficiencies contained in the primary Goldstein reference in that it has no teaching with respect to the use of "several frames" in the method disclosed in Goldstein. As such, it is respectfully submitted that the presently claimed invention is patentably distinguishable over Goldstein in view of Fujimura et al.

The Examiner is respectfully requested to reconsider the present application and to pass it to issue.

Respectfully submitted,

  
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Encl: Marked-Up Substitute Specification  
Clean Substitute Specification  
Replacement Abstract  
Replacement Drawing Sheets for Figures 1 and 5  
Postal Card

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## IMAGE QUALITY CORRECTING CIRCUIT

## TECHNICAL FIELD OF THE INVENTION

The present invention relates to an image quality correcting circuit for the image quality correction (e.g., tone correction of ~~the~~an image, depending on the content of the image, in the cases where ~~the~~ display panels such as ~~the~~a plasma display panel (PDP), ~~the~~ liquid crystal display panel (LCD) and the like are used.

## BACKGROUND ART

~~The~~A conventional image quality correcting circuit, as is shown in Fig. 1, is designed so that the average image level (APL) for every 1 frame (or 1 field) is detected by ~~the~~an average value computer 10, and a corresponding correcting data is read out from ~~the~~a ROM 14 according to the APL, which serves as an address, in order to correct the inputted video signal, for being outputted from ~~the~~an output terminal 18, by ~~the~~an image quality corrector 16 according to ~~the~~an input/output conversion characteristic curve corresponding to the correcting data. The APL is obtained, for example, by dividing the sum of the products of the total number of display dots of each frame (or each field) and the occurrence frequency (number of times of occurrence) distribution of each luminance level by the total number of the dots.

However, according to the conventional case as is shown in Fig. 1, since the image quality correcting data is based on the APL, the display quality of the image can be improved where the brightness is evenly distributed in a given image but cannot be improved according to the content of the image because of a problem, that is, the lack of the consideration of the luminance level histogram (the luminance level occurrence frequency distribution).

For example, assume that there are case 1 of the luminance level occurrence frequency distribution where the luminance levels on the bright side are predominant as shown in Fig. 2(a), and case 2 of the luminance level occurrence frequency distribution where the luminance levels on the dark side is predominant as shown in Fig. 2(b). Assuming that the APL's are equal irrespective of the different luminance levels, there occurs a problem that, in the case shown Fig. 2(a), the resolution on the bright side becomes poor, while, in the case shown in Fig. 2(b), the resolution on the dark side becomes poor. Especially, as shown in Fig. 3, in the case where the luminance level occurrence frequency distribution is predominant in ~~ana~~ a narrow area on the dark side where the luminance level is low, this gives rise to a problem that the correcting characteristic curve tends to have an extremely inclined portion, causing the brightness of the image to become greater than necessary and the resultant decline of the resolution of the image on the bright side. The same is true of the case where the luminance level occurrence frequency distribution is predominant on the brighter side of the image.

In order to resolve the above-mentioned problems, the present applicant has already proposed ~~thea~~ the video signal correcting circuit as is shown in Fig. 5 under the Laid-Open Japanese Patent Application No. 8-23460. According to the proposed circuit, the inputted video signals SO, comprising analog R (Red), G (Green) and B (Blue) signals, are converted into digital R, G and B signals through an A/D 20 (Analog/Digital converter) and inputted, as a lower-rank address, to a ROM 22 (Read-Only Memory) for the input/output conversion, that is, the tone correction by the table look-up method. On the other hand, the Y-signal (luminance signal) is generated from the analog R, G and B signals by a matrix circuit 24, and the Y-signal is converted into a digital signal by the A/D 26 for input to a histogram circuit 28. The histogram circuit 28 counts the luminance level occurrence

frequency (distribution) for each of the luminance level range divided into plural ranges (e.g., 4 ranges). A decoder 30 decodes the result of the count by the histogram circuit 28 for input, as a higher-rank address, to the ROM 22 for selecting the tone correcting characteristic data previously stored in the ROM 22, thereby correcting the tones of the inputted digital R, G and B signals to be outputted as the digital R, G and B signals S1.

With the video signal correcting circuit shown in Fig. 5, tone correction according to the occurrence frequency distribution of the luminance level of the inputted video signal can be made, but such a circuit still ~~have~~has a problem that the correcting characteristic corresponding to the occurrence frequency of each luminance level cannot be obtained.

The present invention is made in order to solve the above problems of the prior art and is intended to provide an image quality correcting circuit applicable for the correction of all kinds of ~~image~~images based on a best correcting characteristic corresponding to the occurrence frequency of each luminance level.

#### DISCLOSURE OF THE INVENTION

A first embodiment of the present invention is an image quality correcting circuit comprising a mean value computer 10 for computing the mean value of the luminance levels of ~~every~~all the plural picture elements of the video signal inputted to a video signal input terminal 12, a counter 13 for counting the occurrence frequency of each of plural luminance levels counted by the mean value computer 10, a linear interpolator 15 for providing a correcting characteristic curve by ~~the~~ linear interpolation based on the counted value output point of the occurrence frequency counter 13, and an image quality corrector 16 for correcting the inputted video signal according to the correcting characteristic curve.

In such a composition, the video signal inputted to a video signal input terminal 12 is processed by the mean value computer 10 for determining the mean value of (luminance levels) of plural picture elements for output, thereby computing the occurrence frequency of each luminance level. The linear interpolator 15 graphically provides a linear interpolation characteristic line consisting of a series of continuous segments with the luminance levels plotted on the y-axis and the occurrence frequency plotted on the x-axis.

The image quality corrector 16 processes the video signal inputted from the video signal input terminal 12 to correct the image quality according to the correcting characteristic line provided by the linear interpolator and outputs the corrected video signal from the video signal output terminal 18.

The second embodiment of the present invention is an image quality correcting circuit comprising the counter 13 for counting the occurrence frequency of the plural luminance levels sampled from the video signal inputted to the video signal input terminal 12 for each of the predetermined levels, a correcting curve generator 25 for generating a new correcting curve based on the data outputted from the counted value output point of the luminance level occurrence frequency counter 13 and the data of the predetermined set points inserted among the counting points, and the image quality corrector 16 for correcting the inputted video signal according to the correcting curve provided by the correcting curve generator 25.

In such a composition, the data of every other occurrence frequency of (the luminance level) of the video signal inputted to the video signal input terminal 12 is used, and, on the other hand, the set data corresponding to the predetermined luminance levels plotted on the straight line connecting the graphic start point and end point is inputted so that these data can be rearranged in the order of the luminance level to interpolate one another for generating a



Bezier curve. The video signal inputted from the video signal input terminal 12 is processed for the image quality correction according to the Bezier curve and outputted from the video signal output terminal 18.

The third embodiment of the present invention comprises an occurrence frequency counter for counting the occurrence frequency of the luminance level of each picture element of the inputted video signal within N number of frames (N = any integer of 1 or more) for every plural set level ranges, a variation controller for controlling, for output, the variation of the counted value of the occurrence frequency counter to a variation within a period ranging plural times of the N-frame period, ~~thea~~ a linear interpolator for providing a correcting characteristic line by the interpolation based on the counted value outputted from the variation controller, and ~~thean~~ an image quality corrector for correcting the inputted video signal by the linear interpolator according to the correcting characteristic line provided by the ~~linear~~ linear interpolator.

In such a composition, when the inputted video signal is then inputted to the luminance level occurrence frequency counter, the occurrence frequency of the luminance level of each picture element within N number of frames is counted by the occurrence frequency counter for every plural set level ranges. Since the variation of the counted value of the occurrence frequency counter is controlled to the variation during a period ranging plural times of the N-frame period by the variation controller, the variation of the correcting characteristic line generated by the linear interpolator is also controlled. The image quality corrector ~~by~~ correcting the inputted video signal according to the variation-controlled correcting characteristic line.

The fourth embodiment of the present invention comprises a counter for counting the occurrence frequency of the luminance level of each picture element within N frames for every plural set level range, a variation controller for

controlling, for output, the variation of the counted value of the occurrence frequency counter to the variation during a period ranging plural times of the N number of frames, a correcting curve generator for generating a new correcting curve based on the counted value outputted from the variation controller and the predetermined set value, and an image quality corrector for correcting the inputted video signal according to the correcting curve generated by the correcting curve generator.

In the above composition, when the video signal is inputted to the luminance level occurrence frequency counter, the occurrence frequency of the luminance level of each picture element is counted for every plural set level range. Since the variation of the counted value of the occurrence frequency counter is controlled to the variation during the period ranging plural times of the N-frame period and inputted to the correcting curve generator, the variation of the correcting curve generated by the correcting curve generator is also controlled. For the image quality correction processing, the image quality corrector corrects the inputted video signal according to the variation-controlled correcting curve.

In order to simplify the composition of the occurrence frequency counter, a mean value computer for computing the mean value of the luminance levels for every m number of picture elements is provided so that the occurrence frequency counter counts the occurrence frequency of the luminance level for every predetermined plural set level range.

In order to simplify the composition of the occurrence frequency counter by eliminating the adder, the occurrence frequency counter is made to comprise a plurality of discriminators for determining whether the luminance level of each picture element corresponds to each of the plural set level ranges, a plurality of first counters for counting the frequency of discrimination made by each of the discriminators, a plurality of comparators for comparing the

counted value of the first counter with a predetermined referenced value and for clearing the first counter by the output from the comparator, and a plurality of second counters for counting the frequency of the output from the comparator to determine the occurrence frequency (of the luminance level).

In order to simplify the composition of the occurrence frequency counter by eliminating the adder, the occurrence frequency counter comprises a plurality of discriminators for determining whether or not the luminance level computed by the mean value computer corresponds to each of the plural set level ranges, a plurality of first counters for counting the frequency of discrimination made by each discriminator, a plurality of comparators for comparing the counted value of the first counter and the predetermined reference value to clear the first counter by the output from the first comparator, and a plurality of second counters for counting the output frequency of the comparator to determine the occurrence frequency.

In order to simplify the composition of the variation controller, the variation controller comprises a difference detector, a constant multiplier, an adder and an N-frame delayer so that the difference detector outputs the difference between the counted value of the occurrence frequency counter and the output from the N-frame delayer; the constant multiplier outputs the product of the output of the difference detector and  $1 \times X$  ( $X =$  an integer of 2 or more); the adder adds the outputted value from the constant multiplier to the outputted value of the N-frame delayer; the N-frame delayer delays the sum obtained by the adder by N frames for output not only as the output to the difference detector and the adder but also as the variation-controlled output.

The fifth embodiment of the present invention is an image quality correcting circuit comprising the occurrence frequency counter 13 for counting the occurrence frequency of the plural luminance levels sampled from the video signal inputted to the

video signal input terminal 12 as to each of the predetermined luminance levels, a correcting characteristic point control circuit 29 for selectively outputting an upper limit value when the counted value of the correcting characteristic point outputted from the occurrence frequency counter 13 is greater than the predetermined upper limit value or outputting a lower limit value when the value is smaller than the (predetermined) lower limit value or outputting the counted value when the counted value is within the range from the upper limit value and the lower limit value, a correcting curve generator 48 for generating a correcting curve according to the output from the correcting control point control circuit 13, and ~~the an~~ an image quality corrector 16 for correcting the inputted video signal according to the correcting characteristic curve generated by the correcting curve generator 48, the image quality correcting circuit being further designed so that the upper limit value and the lower limit value of the counted value of the correcting characteristic point are set to an  $+w$  and an  $-w$  so as to linearly vary respectively.

The sixth embodiment of the present invention is an image quality correcting circuit according to the fifth embodiment, wherein the upper limit value and the lower limit value are set to the upper limit value  $YH_n$  and the lower limit value  $YL_n$ , varying around the middle portion of a quadratic curve passing the graphic start point and graphic end point of a correcting characteristic diagram plotted on the x-axis, representing the inputted luminance level, and the y-axis, representing the outputted luminance level, of a rectangular coordinates.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of a conventional image quality correcting circuit.

Fig. 2 is a video signal luminance level occurrence frequency distribution diagram, wherein (a) represents a case where the luminance levels concentrate mainly around the mean

value, while (b) represents a case where the luminance levels concentrate mainly within range of low values.

Fig. 3 is an occurrence frequency distribution diagram showing the luminance levels of the video signals concentrating within a narrow range on the side of low levels.

Fig. 4 shows a correcting characteristic line according to the case of Fig. 3.

Fig. 5 shows another example of ~~the~~a conventional image quality correcting circuit.

Fig. 6 is a block diagram showing the whole image quality correcting circuit as the first embodiment of the present invention.

Fig. 7 is a block diagram showing the detail of the occurrence frequency counter 13 shown in Figs. 6, 9, 12 and 15.

Fig. 8 is a correcting characteristic line diagram of the first embodiment of the present invention.

Fig. 9 is a diagram showing the whole of the image quality correcting circuit as the second embodiment of the present invention.

Fig. 10 is a correcting characteristic diagram according to the second embodiment of the present invention.

Fig. 11 is ~~an other~~another correcting characteristic diagram according to the second embodiment of the present invention.

Fig. 12 is a block diagram showing the whole of the image quality correcting circuit as the third embodiment of the present invention.

Fig. 13 is a block diagram showing the detail of the variation controller 31 shown in Fig. 12.

Fig. 14 is a time chart illustrating the function of the variation controller 31, shown in Fig. 13.

Fig. 15 is a block diagram showing the whole of the image quality correcting circuit as the fourth embodiment of the present invention.

Fig. 16 is a block diagram showing the whole of the image quality correcting circuit as the fifth embodiment of the present invention.

Fig. 17 is a correcting characteristic line diagram according to the fifth embodiment of the present invention.

Fig. 18 is a block diagram showing the image quality correcting circuit as the sixth embodiment of the present invention.

Fig. 19 is a correcting characteristic line diagram according to the sixth embodiment of the present invention.

#### BEST MODE FOR CARRYING OUT THE INVENTION

The image quality correcting circuit as the first embodiment of the present invention will be described in the following referring to Figs. 6, 7 and 8.

In Fig. 6, numeral 12 represents the video signal input terminal whereto the video signal is to be inputted; 10, the mean value computer for computing the mean value of the luminance levels at m number of points (m = an integer of 2 or more); 13, the occurrence frequency counter for sequentially counting the occurrence frequency of previously set different luminance levels beginning from 0 level; 11, the comparison reference value input terminal; 15, the linear interpolator for obtaining correcting characteristic point based on the occurrence frequency data; 16, the image quality corrector for correcting image quality based on the linear interpolation; 18, the video signal output terminal for outputting a corrected video signal.

Fig. 7 is a circuit diagram of the occurrence frequency counter showing a greater detail.

The mean value computer 10 is designed to compute the ~~means~~mean value of, for example, 16 luminance levels, thereby reducing the number of bits for the subsequent occurrence frequency counter 13.

The occurrence frequency counter 13 comprises, for example, 16 discriminators 17<sub>0</sub>, 17<sub>1</sub>, ... 17<sub>15</sub>, the first counters

19<sub>0</sub>, 19<sub>1</sub>, ... 19<sub>15</sub> respectively and sequentially connected to the discriminators, the comparators 21<sub>0</sub>, 21<sub>1</sub>, ... 21<sub>15</sub>, the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>15</sub>, the comparison reference value input terminal 11 connected to the other input sides of the comparators; wherein the outputs from the comparators 21<sub>0</sub>, 21<sub>1</sub>, ... 21<sub>15</sub> are returned, as clear signals, to the first counters 19<sub>0</sub>, 19<sub>1</sub>, ... 19<sub>15</sub> in the preceding stage, while the outputs of the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>15</sub> are sent to the linear interpolator 15.

The function of the first embodiment having the above-mentioned composition will be described in the following.

The video signal inputted to the video input terminal 12 is processed by the mean value computer 10 to be outputted as the mean value of the luminance levels of 16 picture elements.

The mean value is inputted to the discriminators 17<sub>0</sub>, 17<sub>1</sub>, ... 17<sub>15</sub>, which correspond to respective luminance levels, in order to be determined whether it corresponds to one of the luminance levels. More particularly, the total of the occurrence frequencies within 1 frame is assumed to be 255, and the luminance level is divided into 16 levels for detection. The discriminator 17<sub>0</sub> determines whether (the luminance level) corresponds to (either) the level 0 or the level 1; the discriminator 17<sub>1</sub> determines whether (the luminance level) corresponds to one of the level 0 through the level 2; the same rule applies until it is determined whether (the luminance level) corresponds to one of the 0-level through the level 16. In this way, it is determined whether each of all the luminance levels corresponds to (one of) the level 0 through the level concerned. When the (luminance level) corresponds to (one of the set levels) the occurrence frequency is counted by one of the subsequent first counters 19<sub>0</sub>, 19<sub>1</sub>, ... 19<sub>15</sub>.

The occurrence frequencies counted by the first counters 19<sub>0</sub>, 19<sub>1</sub>, ... 19<sub>15</sub> are respectively inputted to the one ~~side~~side of the subsequent comparators 21<sub>0</sub>, 21<sub>1</sub>, ... 21<sub>15</sub>. The output from the comparison reference value input terminal 11 is inputted

to the other ~~side~~side (of the comparators). Therefore, when the occurrence frequency counted by each of the first counters  $19_0, 19_1, \dots 19_{15}$  is greater than the comparison reference value, the counting is carried out by the second counters  $23_0, 23_1, \dots 23_{15}$ , and the first counters  $19_0, 19_1, \dots 19_{15}$  are cleared.

When the comparison reference value from the comparison reference value input terminal 11 exceeds the number obtained by dividing the number of samples within 1 frame by  $m$ , the number of samples used for computing the mean value (of the luminance levels) by the mean value computer 10, the equation given below is used so that the value of the second counter  $23_{15}$  (correcting characteristic point) becomes 255 (FFH).  
Comparison reference value = (Total number of picture elements within 1 frame/ $m$ )/FFH =  $w$  (Number of picture elements in horizontal direction)  $\times$   $h$  (Number of picture elements in vertical direction)  $\div 16 \div 255$

The occurrence frequencies counted by the second counters  $23_0, 23_1, \dots 23_{15}$  are assumed to be as given below.

c0: Occurrence Frequency of the levels within 00-10 (0F: Hexadecimal number system) counted by the second counter  $23_0$ .

c1: Occurrence Frequency of the levels within 00-20 (1F: Hexadecimal number system) counted by the second counter  $23_1$ .

cE: Occurrence Frequency of the levels within 00-F0 (EF: Hexadecimal number system) counted by the second counter  $23_{15}$ .

The occurrence frequencies counted by these second counters  $23_0, 23_1, \dots 23_{15}$  respectively can be outputted as the correcting characteristic points when graphically represented with the luminance levels plotted on the x-axis and the occurrence frequencies plotted on the y-axis as shown in Fig. 8.

The data covering 16 levels obtained by adding each of the occurrence frequencies  $c_0, c_1, \dots c_E$  to the start point 00 and the total number of occurrence frequencies (a fixed value) is sent to the linear interpolator 15, where the occurrence frequencies 00,  $c_0, c_1, \dots c_E$  are sequentially connected with segments which are linearly interpolated to obtain a



correcting characteristic line consisting of continuous segments.

The image quality corrector 16 processes the video signal inputted from the video signal input terminal 12 for correcting image quality according to the correcting characteristic line provided by the linear interpolator 15 and outputs the corrected video signal from the video signal output terminal 18. More particularly, when the luminance level of the video signal inputted from the video signal input terminal 12 is  $x$ , the image quality correction processing is made according to the correcting characteristic line so that the corrected luminance level becomes  $y$  and (the corrected video signal) is outputted from the video signal output terminal 18.

According to the first embodiment of the present invention as is described in the foregoing, an optimum correcting characteristic can be obtained based on the occurrence frequency data of each luminance level, whereby the image quality correction processing adapted to the condition of the image can be made.

Further, in the case of the first embodiment, the number of samples for determining the mean value by the mean value computer 10 is 16; the number of ~~frame~~frames for obtaining the occurrence frequency data by the occurrence frequency counter is 1; the number of luminance levels is 16, but the present invention is not limited to this case.

According to the first embodiment described above, an optimum correcting characteristic adaptable to the occurrence frequency of each luminance level can be obtained, so that the image correction processing can be made suiting any type of image.

Next, the second embodiment of the present invention will be described referring to Figs. 9, 10 and 11.

In Fig. 9, the video signal input terminal 12, the mean value computer 10, the occurrence frequency counter 13, the image corrector 16 and the video signal output terminal 18 are

similar to those of the first embodiment shown in Figs. 6 and 7. The second embodiment is characterized by the correcting curve generator 25 inserted between the appearance frequency counter 13 and the image quality corrector 16, the correcting curve generator being designed for generating a new correcting curve according to the occurrence frequency data of the video signal which is obtained based on the video signal inputted to the video signal input terminal 12 and counted by the occurrence frequency counter 13 through the mean value computer 10 and the set point data provided by the set point data input terminal 27.

The correcting curve generator 25 comprises a circuit designed for generating a Bezier curve passing through the start point 00 and the end point TF and based on ~~the~~ plural points comprising the occurrence frequency and the set point given alternately.

The function of the second embodiment will be described in the following.

(1) Assume that the video signal inputted to the video signal input terminal 12 has a characteristic causing the occurrence frequency to concentrate about the center similarly to the case of the occurrence frequency distribution of case 1. Unlike the case shown in Fig. 8, as the occurrence frequency data to be provided from the occurrence frequency counter 13, c0, c2, c4, c6, c8, cA, cC and cE corresponding to every other levels 10, 30, 50, 70, 90, B0 and F0 are used. These occurrence frequency data indicate that the occurrence frequencies are at relatively low levels as to c0-c6 and c8-cE, while the occurrence frequencies are at relatively high levels between c6 and c8.

Further, T0, T2, T4, T6, T8, TA, TC and TE corresponding to the levels 00, 20, 40, 60, 80, A0, C0 and E0, which are on the straight line connecting the start point 00 and the end point TF, respectively, are inputted, as set data, to the set data input terminal 27.

Re-arranging these in the order of the luminance level results in T0, c0, T2, c2, T4, c4, T6, c6, T8, c8, TA, cA, TC, cC, TE and cE, which become the correcting line comprising the segments as is shown by a dotted line just like the case of the first embodiment.

However, in the case of the second embodiment, when the Bezier curve passing through the start point 00 and end point FF is generated by the correcting curve generator 25 based on the plural points comprising the occurrence frequency points and the set points, which are arranged alternately, it takes an S-shape with its portion representing relatively high (luminance) levels (slightly) swelling upward, while its portion representing relatively low (luminance) levels sagging (slightly) compared with the straight line as is represented by the solid line in Fig. 10.

The image quality corrector 16 processes the video signal inputted from the video signal input terminal 12 in order to correct the image quality according to the correcting curve provided by the correcting curve generator 25 and outputs (the corrected video signal) from the video signal output terminal 18.

(2) Assume that the video signal inputted to the video signal input terminal 12 has a characteristic as is represented by the occurrence frequency distribution pattern 2 characterized by the predominance of relatively low (luminance) levels as is shown in Fig. 2(b). Such a case indicates that occurrence frequencies are small respectively within the ranges of c0-c2 and c4-cE, while the occurrence frequency is relatively large between c2 and c4.

Similarly to the above case, when the Bezier curve, passing through the start point 00 and the end point TF and generated by the correcting curve generator 25 based on (the set points) re-arranged in the order of T0, c0, T2, c2, T4, c4, T6, c6, T8, c8, TA, cA, TC, cC, TE and cE, is generated, it takes a shape with its portion representing high (luminance) levels ~~is being~~ almost straight, while its portion

representing low (luminance) levels is slightly saggy as is represented by a solid line shown in Fig. 11 compared with the straight line between the start point 00 and the end point TF.

The image quality corrector 16 processes the video signal inputted from the video signal input terminal 12 to correct the image quality according to the correcting curve provided from the correcting curve generator 25 and outputs the corrected video signal from the video signal output terminal 18.

In the case of the above embodiment, the set point data from the set point data input terminal 27 is sampled from the straight line connecting the start ~~point~~point 00 and the end point TF, but the sampling method is not limited to this case; for instance, as in the case of the characteristic line represented by the solid line in Fig. 10, the contrast between the bright portion and the dark portion of the image can be emphasized by sampling the set points from the S-shape curve including the upwardly swelled portion representing the higher (luminance) levels and the ~~saggy~~sagging portion representing the lower (luminance) levels, or the contrast between the bright portion and the dark portion can be weakened by using the set points having inverse ~~characteristic~~characteristics.

Further, the present embodiment is not limited to the case where the appearance frequency data and the set point data are arranged alternately; for instance, the appearance frequency data and the set point data may be arranged at a ratio of 2 to 1 to emphasize the video signal data, or ~~the both~~ may be arranged at a ratio of 1 to 2 to emphasize the set point data.

As described in the foregoing, according to the second embodiment of the present invention, the optimum correcting characteristic can be obtained according to the appearance frequency data for each (luminance) level to make image quality correction processing applicable to any kind of image. Further, by selecting any given points of a correcting

characteristic curve, the characteristic curve can be varied according to the purpose or preference.

Next, an image quality correcting circuit as the third embodiment will be described referring to Figs. 8 through 12.

In Fig. 12, those parts common to those shown in Fig. 6 are assigned common numerals and letters and thus the descriptions thereof are omitted here.

In Fig. 12, 12 denotes the video signal input terminal; 10, the mean value computer; 13, the occurrence frequency counter; 11, reference value input terminal; 15, linear interpolator; 16, image quality corrector; 18, video signal output terminal; 31, variation controller.

The variation controller 31 comprises the 15 variation controllers 31<sub>0</sub>, 31<sub>1</sub>, ... 31<sub>14</sub>; the variation controller 31<sub>0</sub> comprises the (difference detectors) 33<sub>0</sub>, constant multiplier 35<sub>0</sub>, adder 37<sub>0</sub> and N-frame delayer 39<sub>0</sub>; the variation controller 31<sub>1</sub> comprises the (difference detectors) 33<sub>1</sub>, constant multiplier 35<sub>1</sub>, adder 37<sub>1</sub> and N-frame delayer 39<sub>1</sub>, and others comprise similar parts; the variation controller 31<sub>14</sub> comprises the (difference detector) 33<sub>14</sub>, constant multiplier 35<sub>14</sub>, adder 37<sub>14</sub> and N-frame delayer 39<sub>14</sub>.

The (difference detectors) 33<sub>0</sub>, 33<sub>1</sub>, ... 33<sub>14</sub> output the differences between the occurrence frequencies (counted values) outputted from the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>14</sub> and the output values of N-frame delayers 39<sub>0</sub>, 39<sub>1</sub>, ... 39<sub>14</sub>; the constant multipliers 35<sub>0</sub>, 35<sub>1</sub>, ... 35<sub>14</sub> multiplies the output values of the difference detectors 33<sub>0</sub>, 33<sub>1</sub>, ... 33<sub>14</sub> by the coefficient 1/X (X = an integer of 2 or more, e.g., X = 2) to output the product; the adders 37<sub>0</sub>, 37<sub>1</sub>, ... 37<sub>14</sub> and the output values of the constant multipliers 35<sub>0</sub>, 35<sub>1</sub>, ... 35<sub>14</sub> to the output values of the N-frame delayers 39<sub>0</sub>, 39<sub>1</sub>, ... 39<sub>14</sub>; the N-frame delayers 39<sub>0</sub>, 39<sub>1</sub>, ... 39<sub>14</sub> not only delay the sums obtained by the adders 37<sub>0</sub>, 37<sub>1</sub>, ... 37<sub>14</sub> by N frames for output to the difference detectors 33<sub>0</sub>, 33<sub>1</sub>, ... 33<sub>14</sub> and the adders 37<sub>0</sub>, 33<sub>1</sub>, ... 33<sub>14</sub> but also output to the linear interpolator 15. The occurrence frequency to be outputted from the second counter

23<sub>15</sub> in the appearance counter 13 is directly outputted to the linear interpolator 15 without using the variation controller 13.

The function of the third embodiment having the above composition will be described referring to Fig. 14 and Fig. 8.

N may be any integer of 1 or more, m, any integer of 2 or more, X, any integer of 2 or more, but, for convenience, the explanation will be made as to the cases where N = 1, m = 16 and X = 2.

(1) The video signals inputted to the video signal input terminal 12 are processed by the mean value counter 10 and outputted sequentially as the mean values of every 16 luminance levels.

(2) When the mean value computed by the mean value computer 10 is inputted to the occurrence frequency counter 13, the occurrence frequency counter 13 functions similarly to the case of the first embodiment shown in Figs. 6 and 7.

For the convenience of explanation, assume that the occurrence frequencies outputted from the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>15</sub> are c0, c1, ... cE, and cF respectively. In this case, c0, c1, ... cE and cF represent the occurrence frequencies given below.

c0: Occurrence frequency counted by the second counter 23<sub>0</sub> within the level range of 00-10 (10 is a hexadecimal).

c1: Occurrence frequency counted by the second counter 23<sub>1</sub> within the level range of 00-20 (20 is a hexadecimal).

.....

cE: Occurrence frequency counted by the second counter 23<sub>14</sub> within the level range of 00-F0 (F0 is a hexadecimal).

cF: Occurrence frequency (constant value) counted by the second counter 23<sub>15</sub> within the level range of 00-100 (100 is a hexadecimal).

(3) Upon receipt of the 16-level data comprising the occurrence frequencies c0, c1, ... cF outputted from the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>15</sub> and the data of the start point 00 added thereto, the variation controller 31 controls the

variation of each of the occurrence frequencies  $c_0, c_1, \dots c_E$  and  $c_F$  during 1 frame period (case where  $N = 1$ ) to the variation during plural-frame ~~period~~periods (an example of plural times of  $N$  frame) for output, provided that  $c_F$  will not vary, since it ~~being~~is a constant value.

For instance, assuming that the appearance frequency  $c_0$  of the second counter  $23_0$  becomes  $[2], [2], [2], [2], [2], [16], [16], [16], [16], [16], [16]$  and  $[16]$  as shown in Fig. 14(a) and varies abruptly to  $[16]$  from  $[2]$  during the frame period  $FT$  near the point  $t_6$ , as shown in (b) of the same figure, owing to the control function of the variation controller  $31$ , the appearance frequency varies from  $[2]$  to  $[9]$  during the 1-frame period  $FT$  immediately following the point  $t_7$ , the point coming after the lapse of 1-frame period  $FT$  from point  $t_6$ , and further varies to  $[13], [15]$  and  $[16]$  respectively during the 1-frame periods immediately following the points  $t_8, t_9$  and  $t_{10}$  until converging to  $[16]$ . That is, a rapid variation during a single frame period is controlled to ~~amore~~ gentle variations ranging over a 4-frame period.

The control function of the variation controller  $31$  will be explained dividing it into the following paragraphs 1 through 5 and with reference to the circuit diagram shown in Fig. 13.

For ~~the~~ convenience of explanation, assume that the occurrence frequency (correcting characteristic point) of the second counter  $23_0$  is  $P_0$ , and the occurrence frequency (correcting characteristic point) of the  $N$ -frame delayer  $39_0$  is  $PD_0$ .

① As shown in Fig. 14(a), assuming that the  $P_0$  has varied to  $[16]$  from  $[2]$  during the 1-frame period  $FT$  at around the point  $t_6$ ,  $P_0 = 16$  and  $PD_0 = 2$  during this 1-frame period  $FT$ , and so the occurrence frequency outputted from the variation controller  $31_0$  becomes  $[2]$ .

In this case, the output  $(P_0 - PD_0)$  of the difference detector  $33_0$  becomes  $14 (= 16 - 2)$ ; the output  $\{(P_0 - PD_0) \times 1/2\}$

of the constant multiplier 350 is  $7 (= 14/2)$ ; the output  $\{PD0 + (P0 - PD0)1/2\}$  of the adder 37<sub>0</sub> is  $9 (= 2 + 7)$ .

② During the 1-frame period near the point t7 immediately following the laps of the 1-frame period Ft from the point t6, the data obtained by delaying by 1 frame the output of the adder 37<sub>0</sub> becomes the output (i.e., PD0) of the N-frame delayer 39<sub>0</sub>, so that the occurrence frequency outputted from the variation controller 31<sub>0</sub> becomes [9].

In this case, the output  $(P0 - PD0)$  of the difference detector 33<sub>0</sub> is  $7 (= 16 - 9)$ ; the output  $\{(P0 - PD0) \times 1/2\}$  of the constant multiplier 35<sub>0</sub> is 4 ( $= 7/2$  with decimals rounded off); the output  $\{PD0 + (P0 - PD0) \times 1/2\}$  is 13 ( $= 9 + 4$ ).

③ During the 1-frame period FT immediately following the point t8 after the lapse of 1-frame period from the point t7, similarly to the case of the paragraph 2, the occurrence frequency outputted from the variation controller 31<sub>0</sub> becomes 13.

In this case, the output of the adder 37<sub>0</sub> becomes  $15 (= 13 + 2)$ , similarly to the case described in the above paragraph ②.

④ During the 1-frame period FT immediately following the point t9 after the lapse of the 1-frame period TF from the point t8, similarly to the case described in the above paragraph ②, the appearance frequency outputted from the variation controller 31<sub>0</sub> becomes 15.

In this case, the output of the adder 37<sub>0</sub>, similarly to the case described in the above paragraph ②, becomes 16 ( $= 15 + 1$ ).

⑤ During the 1-frame period FT immediately following the point t10 after the lapse of the 1-frame period FT from the point t9, the occurrence frequency outputted from the variation controller 31<sub>0</sub> becomes [16] similarly to the case described in the above paragraph ②.

In this case, the output of the adder 37<sub>0</sub> becomes 16 ( $16 + 0$ ) similarly to the case described in the above paragraph ②.



(4) The variations of the appearance frequencies  $c_1, \dots, c_E$  of other second adders 37<sub>0</sub> are also made gentler for output because of the controlling effect of the variation controller 31.

Applying the above case to the circuit shown in Fig. 13, the appearance frequencies of the second counters 23<sub>1</sub>, ... 23<sub>14</sub> become  $P_1 (=c_1), \dots, P_{14} (=c_E)$ , and, when the output values of the N-frame delayers 39<sub>1</sub>, ... 39<sub>14</sub> become PD<sub>1</sub>, ... PD<sub>14</sub>, and the  $P_1, \dots, P_{14}$  vary sharply during a certain 1-frame period, the variations of the corresponding PD<sub>1</sub>, ... PD<sub>14</sub> are controlled to milder variations during plural-frame period.

(5) When the variations of the appearance frequencies  $c_0$  ( $=P_0$ ),  $c_1$  ( $=P_1$ ), ...  $c_E$  ( $=P_{14}$ ) of the second counters 23<sub>0</sub>, 23<sub>1</sub>, ... 23<sub>14</sub> controlled to milder variations are sent to the linear interpolator 15, the linear interpolator 15 linearly interpolates the variations by sequentially connecting the controlled appearance frequencies 00,  $c_0, c_1, \dots, c_E$  and  $c_F$  with a straight line to generate a correcting characteristic line.

For instance, comparing with a case where the correcting characteristic line obtained by the conventional linear interpolator, not provided with the variation controller 31, as is shown in Fig. 6 has varied abruptly, just like the case of the correcting characteristic line U1 indicated by a dotted line and the correcting characteristic line U2 indicated by a solid line shown in Fig. 8, during 1-frame period, in the case of the circuit provided with the variation controller 31 according to the present invention, the variation from the correcting characteristic line U1 to the similar line U2 can be made more gently over a plural-frame period.

That is, since the variations of the appearance frequencies of  $c_0, c_1, \dots, c_E$  become gentler owing to the function of the variation controller 31, the correcting characteristic line generated by the linear interpolator 15 varies gently over a plural-frame period (e.g., period ranging over 4-6 frames) and converges to U2.

In this case, the appearance frequency  $c_0$  varies gently ranging over a 4-frame period as described in the parts 1 - 5, while the appearance frequencies  $c_1, \dots, c_E$  vary gently ranging over a 4-frame period or over plural ranges other than 4 frames (e.g., 5 or 6 frames) depending on the amount of variation.

In Fig. 8, the correcting characteristic line  $U_1$  corresponds to the correcting characteristic line ranging from the point  $t_6$  to the point  $t_7$ , the point after the lapse of 1-frame period from the point  $t_6$ , and the  $c_0$  (the appearance frequency by the second counter 23<sub>0</sub>) on this correcting characteristic line  $U_1$  corresponds to [9] described in the above paragraph ②.

(6) The image quality corrector 16 processes for image quality correction the video signal inputted from the video signal input terminal 12 according to the correcting characteristic line and outputs (the corrected video signal) from the video signal output terminal 18. More specifically, when the luminance level of the video signal inputted from the video signal input terminal 12 is  $x$ , the image quality correction processing is made according to the correcting characteristic line so that the luminance level is corrected to  $y$ , and (the corrected video signal) is outputted from the video signal output terminal 18.

According to the third embodiment of the present invention, not only the image quality correction processing can be made according to the optimum correcting characteristic based on the appearance frequency of the luminance level of each picture element within  $N$  frames but also the image quality correction without image quality deterioration can be made preventing a large variation of the luminance level appearance frequency distribution occurring at the time of the switching of the image or during the display of the moving image.

Next, the image quality correcting circuit as the fourth embodiment of the present invention will be described referring to Fig. 15.

In Fig. 15, the video signal input terminal 12, the mean value computer 10, the appearance frequency counter 13, the image quality corrector 16, the video signal output terminal 18 and the variation controller 31 are similar to those of the embodiment 3 shown in Fig. 12 and Fig. 13. The fourth embodiment is characterized by that a correcting curve generator 25 provided replacing the linear interpolator 15 shown in Fig. 12.

The correcting curve generator 25, inserted between the variation controller 31 and the image quality corrector 16, is designed to generate a new correcting curve based on the occurrence frequency, which is counted by the occurrence frequency counter 13 and whose variation is controlled by the variation controller 31, and the set point data, which is provided from the set point data input terminal 27.

The correcting curve generator 25 is, for example, composed of a circuit designed for generating a Bezier curve passing through the start point 00 and the end point TF based on plural points representing the occurrence frequency and set points arranged alternately.

The function of the fourth embodiment will be explained referring to Figs. 2, 9, 10 and 11.

(1) Assume that the (luminance level) occurrence frequencies of the video signals inputted to the video signal input terminal 12 tend to concentrate almost around the mean value as is represented by the frequency distribution and shown in Fig. 2(a).

As the occurrence frequencies by the occurrence frequency counter 13, c0, c2, c4, c6, c8, cA, cC and cE are used corresponding to the (luminance) levels 10, 30, 50, 70, 90, B0, D0 and F0 which are arranged by skipping every next level similarly to the case of the second embodiment shown in Fig. 2.

T0, T2, T4, T6, T8, TA, TC and TE respectively corresponding to the levels 00, 20, 40, 60, 80, A0, C0 and E0 on the straight line connecting the start point 00 and end point TF are inputted, as the set data, from the data input terminal 27.

In the conventional case where the variation controller 31 is not provided as shown in Fig. 9, the S-shape correcting curve V (Bezier curve), as is shown by a solid line in Fig. 10, is provided, by the correcting curve generator 25, based on the plural points consisting of the occurrence frequency data: c0, c2, c4, c6, c8, cA, cC and cE, and set point data: T0, T2, T4, T6, T8, TA, TC and TE, which are arranged alternately; in such a case, when the occurrence frequencies c0, c2, c4, c6, c8, cA, cC and cE respectively vary sharply, the correcting curve also varies abruptly from the correcting curve V1 to the correcting curve V2 (V1 and V2, not shown).

However, in the case of the embodiment provided with the variation controller 31 shown in Fig. 15, the variations of the occurrence frequencies of the c0, c2, c4, c6, c8, cA, cC and cE during 1 frame are controlled to gentler variations ranging over a plural-frame period, so that the correcting curve V to be generated by the correcting curve generator 25 vary gradually as V1, V1<sub>1</sub>, V1<sub>2</sub>, ..., V2 frame by frame until converging to V2 (V1<sub>1</sub>, V1<sub>2</sub>, not shown).

The image quality corrector 16 processes, for image quality correction, the video signal inputted from the video signal input terminal 12 and outputs the corrected video signal from the video signal output terminal 18.

(2) Assume that the video signals inputted to the video signal input terminal 12 has a (luminance level) occurrence frequency distribution pattern 2 characterized by the concentration within a low level range.

Similarly to the case described in the above paragraph (1), in the case of the second embodiment not provided with the variation controller 31, a correcting curve W (Bezier curve) is generated by the correcting curve generator 25 based

on the plural points consisting of the appearance frequency data: c0, c2, c4, c6, c8, cA, cC and cE and set point data: T0, T2, T4, T6, T8, TA, TC and TE, which are arranged alternately, and, when the appearance frequencies c0, c2, c4, c6, c8, cA, cC and cE respectively vary sharply, the correcting curve W also varies sharply from W1 to W2 (W1 and W2, not shown).

However, in the case of the fourth embodiment provided with the variation controller 31, the variations of the occurrence data: c0, c2, c4, c6, c8, cA, cC and cE during 1-frame period are controlled to the gentler variations ranging over the plural-frame period, so that the correcting curve W also varies gradually from W1, W1<sub>1</sub>, W1<sub>2</sub>, ... W2 through each single-frame period until converging to W2 (W1<sub>1</sub>, W1<sub>2</sub>, not shown).

The image quality corrector 16 processes, for image quality correction, the video signal inputted from the video signal input terminal 12 according to the correcting curve generated by the correcting curve generator 25 and outputs the corrected video signal from the video signal output terminal 18.

In the case of the fourth embodiment, the set point data from the set point data input terminal 27 is sampled from the straight line connecting the start point 00 and the end point TF, but not limited to such a sampling; for example, the set point data may be sampled from the S-shape curve with a portion slightly swelling upward by representing the high levels and a saggy portion representing the low levels, as is given by a solid line in Fig. 10, in order to emphasize the contrast between the brightness and the darkness or in order to weaken the contrast between the brightness and the darkness by sampling the set points having inverse characteristics.

Further, the present embodiment is not limited to the case where occurrence frequency data and the set point data are arranged alternately; the data of the video signal may be emphasized by setting the ratio between the occurrence

frequency data and the set point data for 2 to 1, or the set point data may be emphasized by setting the ratio for 1 to 2 or any other ~~ratio~~ratio.

According to the fourth embodiment described in the foregoing, not only the image quality correction processing can be accomplished according to the optimum correcting characteristic based on the occurrence frequency corresponding to each (luminance) level but also the image quality correction processing can be accomplished without entailing the image quality deterioration even when the occurrence frequencies of luminance levels of the picture elements vary largely. Further, the extreme variation of the correcting curve can be prevented by selectively using the correcting characteristic, or the correcting curve can be varied according to the purposes or preferences.

Concerning the third embodiment and the fourth embodiment, the cases where the mean value computer is provided in order to simplify the composition of the occurrence frequency counter have been explained, but these embodiments are not limited to these cases; these embodiments are also applicable to the cases where the mean value computer is omitted.

The image quality correcting circuit as the fifth embodiment of the present invention will be explained referring to Fig. 16 and Fig. 17.

In Fig. 16, numeral 12 denotes the video signal input terminal whereto the video signal is to be inputted; 10, the mean value computer for computing the mean value of the values at m number ( $m = \text{integer of } 2 \text{ or more}$ ) of points; 13, the occurrence frequency counter for sequentially counting the occurrence frequencies set to different values beginning from 0 level and ranging to N number (any integer of 1 or more) of frames; 29, the correcting characteristic point control circuit for controlling the correcting characteristic point by setting the upper limit value ( $a_n + w$ ) and the lower limit value ( $a_n - w$ ); 48, the correcting curve generator for

generating the correcting curve according to the controlled correcting characteristic points; 49, set point data input terminal for inputting the set point data; 16, the image quality corrector for correcting the image quality according to the generated correcting curve; 18, the video signal output terminal for outputting the corrected video signal.

The correcting characteristic point control circuit 29 comprises a control range comparator 43, a control range setter 46, a control counter for specifying the order of processing and a correcting characteristic point selector 47; the control range comparator 43 comprises the upper limit comparator 41a for comparing the uncorrected correcting characteristic point data ( $P_n$ ) with the upper limit value ( $a_n + w$ ) at that characteristic point and the lower limit comparator 42a for comparing the  $P_n$  with the lower limit value ( $a_n - w$ ) at that characteristic point; the control range setter 46 comprises an upper limit setter 44a for setting the upper limit value ( $a_n + w$ ) and a lower limit setter 45a for setting the lower limit value ( $a_n - w$ ); the correcting characteristic point selector 47 is a circuit for selecting the terminal  $x$  for  $P_n$  of the occurrence frequency counter 13, the terminal  $y$  for the upper limit value ( $a_n + w$ ) of the upper limit setter 44a and the terminal  $z$  for the lower limit value ( $a_n - w$ ) of the lower limit setter 45a.

In this case, as shown in Fig. 17, the straight line, connecting the start point and the end point in ~~a~~rectangular coordinates with its  $x$ -axis representing the output level of the luminance and its  $y$ -axis representing the input level of the luminance, represents an ideal correcting characteristic line in the case where there is no deviation with respect to the occurrence frequencies of the input levels of the inputted video signals, wherein  $w$  represents a certain distribution range around the ideal characteristic line. However,  $a_n + w$  and  $a_n - w$  respectively represent the control set lines representing the upper limit and the lower limit having

certain extent lying parallel to the ideal straight line, as shown by the dotted lines in Fig. 17.

The function of the fifth embodiment having the above-mentioned compositions will be explained.

The video signals inputted to the video signal input terminal 12 are processed by the mean value computer 10 for computing the mean values of the luminance levels of every 16 picture elements at various points, which are to be output sequentially.

This mean value is found on the assumption that the total number of the occurrence frequencies within N number ( $N =$  any integer of 1 or more) be 255 and there are 16 different luminance levels. The detected mean values are counted by the occurrence frequency counter.

The uncontrolled correcting characteristic point data  $P_n$  from the occurrence frequency counter 13 is inputted not only to the contact x of the correcting characteristic point selector 47 but also inputted to the one side of the subsequent upper limit comparator 41a and that of the lower limit comparator 42a as an input. Further, the upper limit value  $a_n + w$  and the lower limit value  $a_n - w$  respectively corresponding to the numbers of the correcting characteristic point data  $P_n$  from the control counter 40 are inputted to the other sides of ~~these~~ the upper limit comparator 41a and the lower limit comparator 42a respectively. Therefore, when  $P_n > a_n + w$ , the upper limit comparator 41a outputs the signal for switching to the contact y of the correcting characteristic point selector 47, and, when  $P_n < a_n - w$ , the lower limit comparator 42a outputs the signal for switching to the contact z of the correcting characteristic selector 47.

For instance, in the case shown in Fig. 17, since  $P_n > a_n + w$  where  $P_n$  is at an extremely high point y, the switching signal is outputted from the upper limit comparator 41a to switch the contact of the correcting characteristic point selector 47 to the contact y and the  $a_n + w$  from the upper limit setter 44a is outputted to the correcting curve



generator 48. The correcting curve generator 48 generates a corrected correcting curve on the upper limit set line P1 shown in Fig. 17 according to the  $a_n + w$  outputted from the upper limit setter 44a to serve as an address and the set point data inputted from the set point data input terminal 49, and the corrected correcting curve is outputted to the image quality corrector 16. The video signal inputted from the video signal input terminal 12 is corrected by the image quality corrector 16 according to the correcting curve, and the corrected video signal is outputted to the output terminal 18. As discussed in the foregoing, the deterioration of the image quality can be prevented by controlling the point  $y$  whose distribution of occurrence frequencies has some dispersion within a set range.

Further, in the case shown in Fig. 17, since  $P_n < a_n - w$  where  $P_n$  is at an extremely low input level point  $z$ , the switching signal is outputted from the lower limit comparator 42a to switch the contact of the correcting characteristic point selector 47 to the contact  $z$ , and the  $a_n - w$  from the lower limit setter 45a is outputted to the correcting curve generator 48. The correcting curve generator 48 generates a corrected correcting curve on the point P3 the lower limit setting line shown in Fig. 17 according to the output  $a_n - w$  to serve as an address from the lower limit setter 45a and the set point data inputted from the set point data input terminal 49, and the corrected correcting curve is outputted to the image quality corrector 16. The image quality corrector 16 corrects the video signal inputted from the video signal input terminal 12 according to the corrected correcting curve, and the corrected video signal is outputted to the output terminal 18. As discussed above, the deterioration of the image quality can be prevented by controlling the  $z$  point whose distribution of occurrence frequency is dispersed within a set range.

Further, since  $a_n + w \geq P_n \geq a_n - w$  where  $P_n$  is at the  $z$  point between the upper limit value and the lower limit value,

the switching signal from the upper limit comparator 41a and the lower limit comparator 42a will not be outputted, and the  $P_n$  from the occurrence counter 13 is outputted to the correcting curve generator 48. The correcting curve generator 48 outputs the uncorrected correcting circuit curve to the image quality corrector 16, and the video signal from the video signal input terminal 12 outputs the uncorrected video signal to the output terminal 18.

As described in the foregoing, according to the fifth embodiment of the present invention, the generation of the correcting characteristic line having an extreme inclination and the image quality deterioration due to the inclination of the occurrence frequency can be prevented to obtain an optimum correcting characteristic.

Next, the sixth embodiment of the present invention will be described referring to Fig. 18 and Fig. 19.

In the case of the fifth embodiment, regardless of the various correcting characteristic ~~point~~points, the range of the upper limit value  $+w$  and the limit of the lower limit value  $-w$  are assumed to be fixed.

In contrast, in the case of the sixth embodiment, in computing the correcting characteristic point based on the occurrence frequency of the input level of the video signal within  $N$  number of frames, the upper limit range and the lower limit range are set for each correcting characteristic point.

The sixth embodiment will be described in detail.

In Fig. 18, the video signal input terminal 12, the mean value computer 10, the occurrence frequency counter 13, the image quality corrector 16, the video signal output terminal 18, the correcting curve generator 48 and the set point data input terminal 49 are similar to those in the case of the fifth embodiment. On the other hand, as shown in Fig. 19, the second embodiment is characterized by the generation of a quadratic curve according to the correcting characteristic points representing the upper limit value  $YH_n$  and the lower limit value  $YLn$  being 0 respectively at the start point 0, the

upper limit value  $YH_n$  increasing gradually with respect to the straight line an until the intermediate position, and the lower limit value  $YLn$  decreasing gradually with respect to the straight line an to pass the end point FF again. As described in the foregoing, the straight line an connects the start point and the end point FF in the correcting characteristic diagram and represents an ideal correcting characteristic line where the occurrence frequencies of the input levels of the inputted video signals are free of the inclination.

The function of the sixth embodiment will be described in the following.

For instance, in the case shown in Fig. 19, since  $P_n > YH_1$  where the uncontrolled correcting characteristic point data  $P_n$  from the occurrence frequency counter 13 is at an extremely high input level point y, the switching signal is outputted from the upper limit comparator 41b to switch the contact of the correcting characteristic point selector 47 to the contact y, and the  $YH_1$  from the upper limit comparator 44b is outputted to the correcting curve generator 48. The correcting curve generator 48 generates a corrected correcting curve at the point P1 of the upper limit set line, shown in Fig. 19, according to the output, serving as an address, of the upper limit setter 44b and based on the set point data inputted from the set point data input terminal 49 and outputs the corrected correcting curve to the image quality corrector 16. The image quality corrector 16 corrects the video signal from the video signal input terminal 12 according to the correcting curve and outputs the corrected video signal to the output terminal 18. In this way, the deterioration of the image quality is prevented by controlling the point y, whose distribution of occurrence frequency is inclined, within the set range.

Further, in the case shown in Fig. 19, since  $P_n < YL_3$  where  $P_n$  is at the point z, at which the input level is extremely low, the switching signal is outputted from the lower limit comparator 42b to switch the contact of the

correcting characteristic selector 47 to the contact z, and the YL3 from the lower limit setter 45b is outputted to the correcting curve generator 48. The correcting curve generator 48 generates a corrected correcting curve at the point P3 on the lower limit set line, shown in Fig. 19, according to the output, serving as an address, from the lower limit setter 45b and based on the set point data inputted from the set point data input terminal 49 and outputs the corrected correcting curve to the image quality corrector 16. The image quality corrector 16 corrects the video signal from the video signal input terminal 12 according to the corrected correcting curve and output the corrected video signal to the output terminal 18. In this way, the deterioration of the image quality is prevented by controlling the z point, whose distribution of occurrence frequency is inclined, within the set range.

Further, since  $YH2 \geq Pn \geq YL2$  where the  $Pn$  is at point x between the upper limit value and the lower limit value, the switching signal will not be outputted from the upper limit comparator 41a and the lower limit comparator 42a, and the  $Pn$  from the occurrence frequency counter 13 is outputted to the correcting curve generator 48. The correcting curve generator 48 outputs an uncorrected correcting curve to the image quality corrector 16, and the video signal from the video signal input terminal 12 is outputted to the output terminal 18 without being corrected.

As described in the foregoing, according to the sixth embodiment, the optimum correcting characteristic adapted to the appearance frequency data of each level, and image quality correction processing suiting any kind of image can be made available. Further, according to this embodiment, not only ~~the~~ an extreme variation of the correcting curve can be prevented according to correcting characteristic point but also the correcting curve can be varied either intentionally or by the preference.

Further, in the cases of the fifth embodiment and the sixth embodiment, the number of samples for obtaining the mean

value to be computed by the mean value computer 10 is set to 16, the number of frame for obtaining the occurrence frequency data from the occurrence frequency counter 13 to 1 frame, and the number of degrees of the luminance level to 16, but the present invention is not limited to these cases.

#### INDUSTRIAL APPLICABILITY

As described in the foregoing, the image quality correcting circuit according to the present invention is not only capable of obtaining the optimum correcting characteristic according to the occurrence frequency data of each (luminance) level so that the image quality correction processing suiting any kind of image but also capable of preventing extreme variation of correcting curve or varying the correcting curve either intentionally or preferentially. Further, even when the distribution of the occurrence frequency of the luminance level varies largely at the time of the switching of the image or when displaying moving images, such extreme variations and resultant image quality deterioration can be prevented by implementing the image quality correction processing according to the present invention.